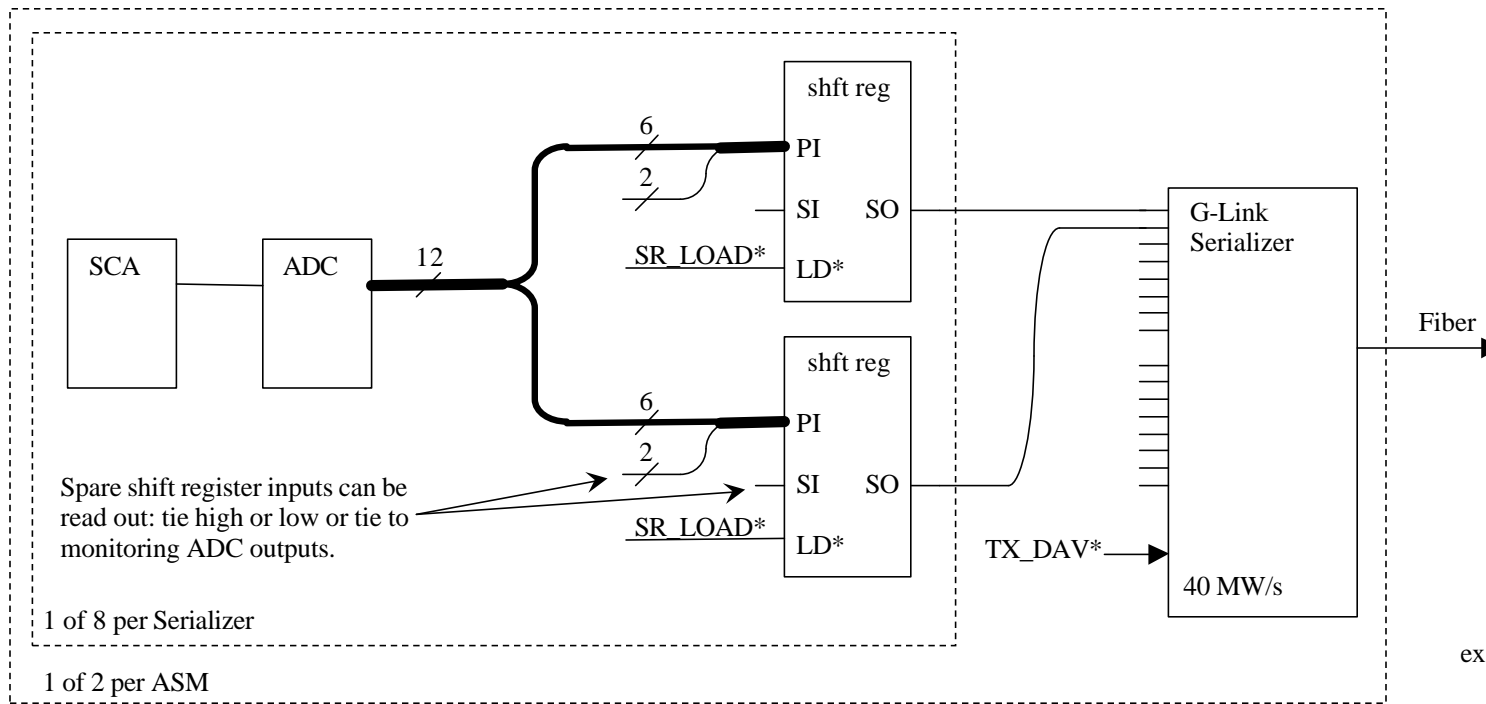
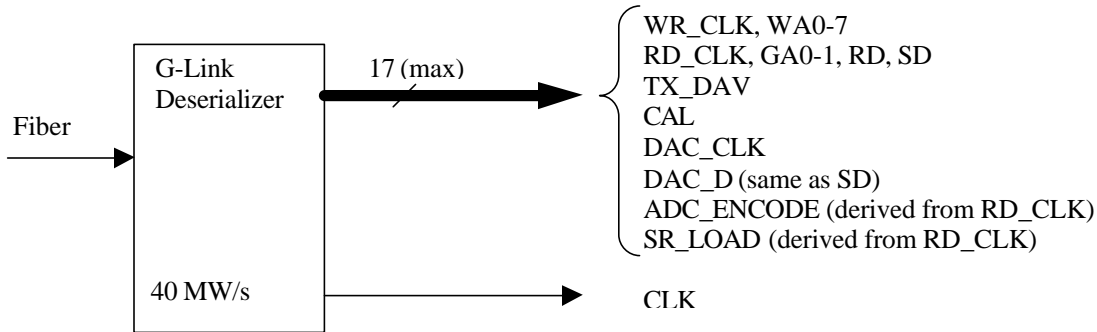
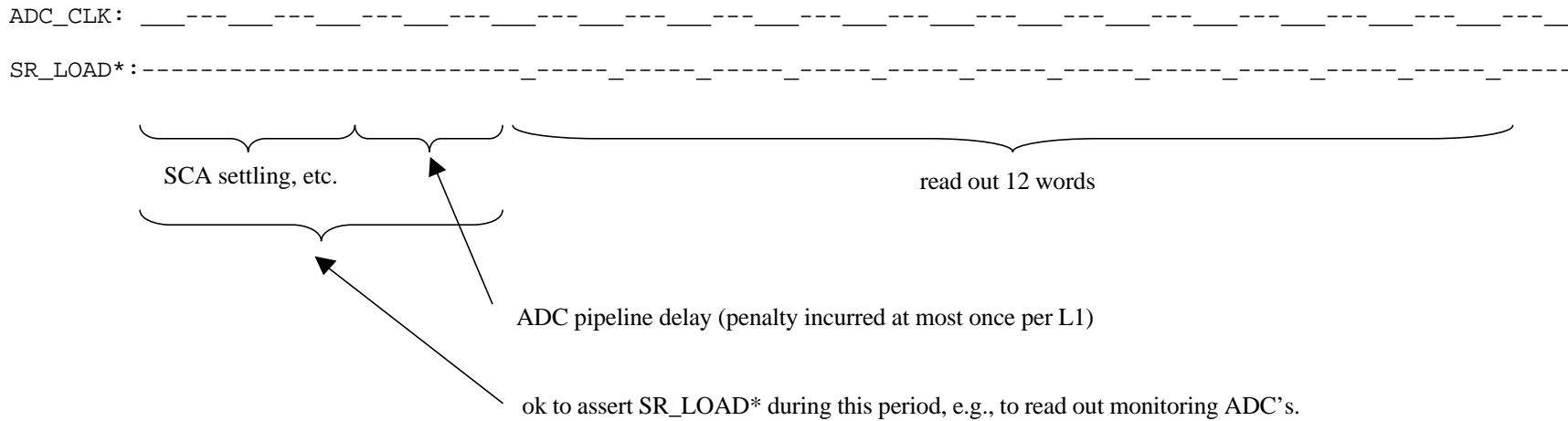


ASM Digital Electronics—Non-Sparsified Data, Dual Data Links





Pros:

- all logic is off-the-shelf
- operates at low frequency: 40 MHz
- 3*32 spare shift register bits are available for sending misc. info
- 16-bit data word allows use of low-power HDMP-1032/1034
- robust: most failures are localized

Cons:

- requires two data fibers per ASM
- supports readout of at most four time samples
- high-fanout nets (e.g., CLK, SR_LOAD*) require buffers
- shift registers and buffers require radiation testing