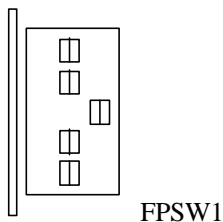


IROD Final Assembly

The following steps are required after IROD automated assembly:

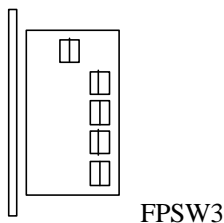
1. Replace XC95144XL-TQ144 at location VMU13 (Flash PLD) with XC95288XL-TQ144 (speed -7c).
2. Install metric press-fit backplane connectors.
3. Install microswitch in lower injector/ejector handle.
4. Install front panel, including upper and lower injector/ejector handles.
5. Solder microswitch black and red wires to PWH3. Trim yellow wire.
6. Install missing TDC (33uF 16V tantalum capacitor) near PWH3.
7. Program the CPLD's. See **Detail A**.
8. Program the flash memory. See **Detail B**.
9. Remove the key pin from the emulator connector (FPH1). The pin can be removed by fatiguing it with a needle nose pliers. To identify the pin, see the emulator pod or a working IROD.
10. Set all DIP switches to their final positions. See **Detail C**.

Detail A, Programming the CPLD's. Set all IROD DIP switches to the OFF position (away from the PCB) except FPSW1. Set FPSW1 as shown:



Connect the Xilinx JTAG pod (not the emulator pod) at FPH2. Power up the crate's 3.3V and 5V. Use iMPACT with chain file **irod_cpld_chain.ipf** to program the CPLD's. Disconnect the JTAG pod.

Detail B, Programming the Flash Memory. Set all IROD DIP switches to the OFF position (away from the PCB) except FPSW3. Set FPSW3 as shown:



Program the flash memory using the rodFlash utility running on the RCC.

Detail C, Final DIP Switch Position. Set all IROD DIP switches to the OFF position (away from the PCB) except FPSW2. Set FPSW2 as shown:

